	Type	L#	Hits	Search Text	DBs	Time Stamp
***************************************	BRS	1.8	168	((multilayer multi adj (layer)) with ceramic with substrate) same electrode and (resin)	USPAT	2009/02/16 19:10
	BRS	 9	229	((multilayer multi adj (layer)) with ceramic with substrate) same electrode and (resin)	US-PGPUB; USOCR; EPRS; EPO; JPO; DERWENT; IBM_TDB	2009/02/16 19:20
0	BRS	1.12	1575	(multilayer multi adj layer) with ceramic with substrate) and (device element component) and (resin insulating insulation dielectric) and (contact pad plug bump ball electrode) and (power ground reference adj voltage)	USPAT	2009/02/16 19:29
4	BRS	1.13	235	12 and overlap\$5	USPAT	2009/02/16 19:29
W.	BRS	14	1025		US-PGPUB; USOCR; FPRS; EPO; JPO; DERWENT: JBM_TDB	2009/02/16 19:38
6	BRS	70. 70. 70.	182	14 and overlap\$5	US-PGPUB; USOCR; FPRS; EPO; JPO; DERWENT; JBM_TDB	2009/02/16 19:39
**	BRS	1.76	18	(((multilayer multi adj layer) with ceramic with substrate) and (device element component) and (resin insulating insulation dielectric) and (contact pad plug bump ball electrode) and (power ground reference adj voltage)).clm.	US-PGPUB	2009/02/16 20:47
***	BRS	1.17		(((multilayer multi adj layer) with ceramic with substrate) and (device element component) and (resin insulating insulation dielectric) and (contact pad plug bump ball electrode) and (power ground reference adj voltage) and overlap\$5).clm.	US-PGPUB	2009/02/16 20:48